
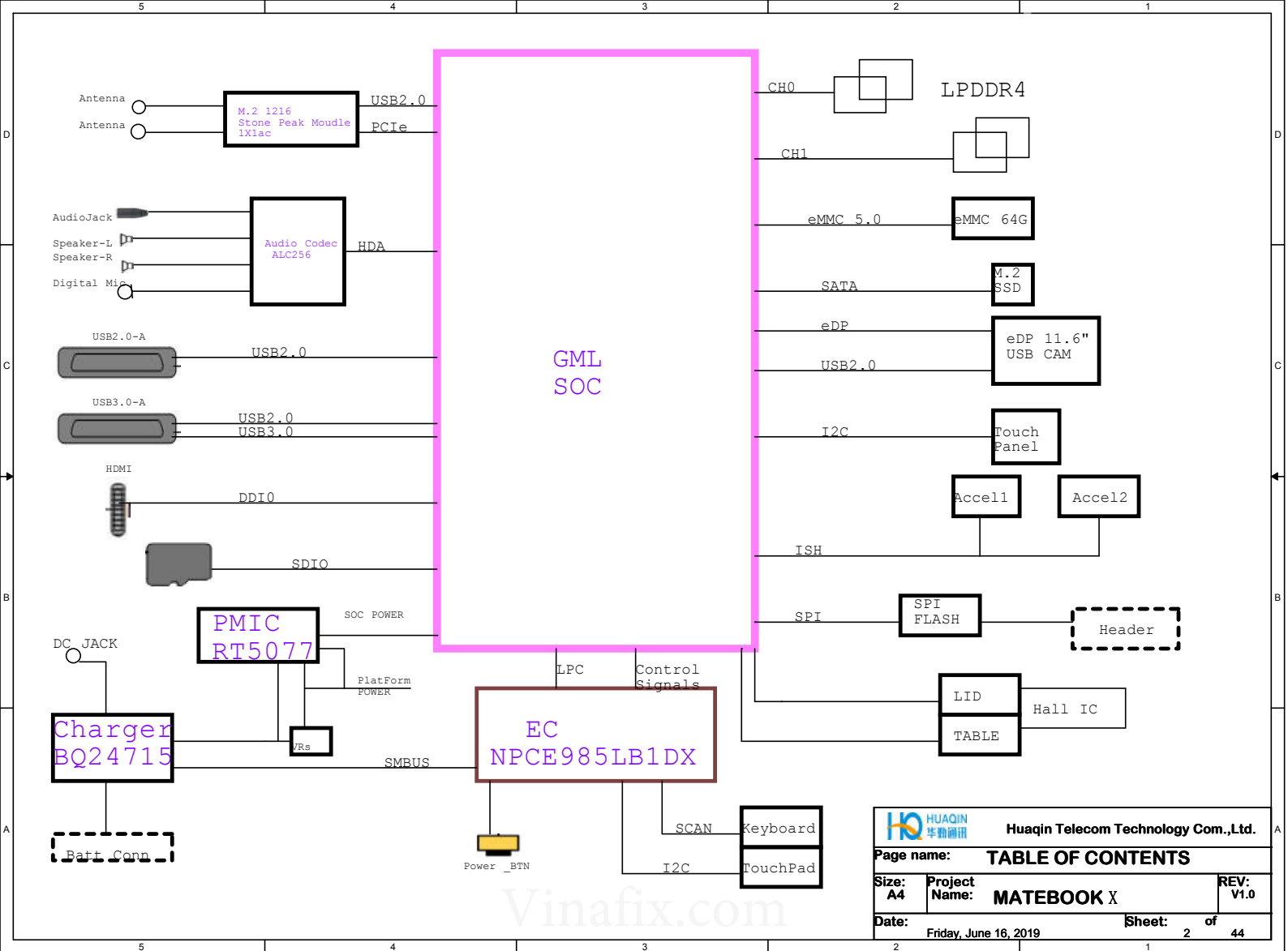



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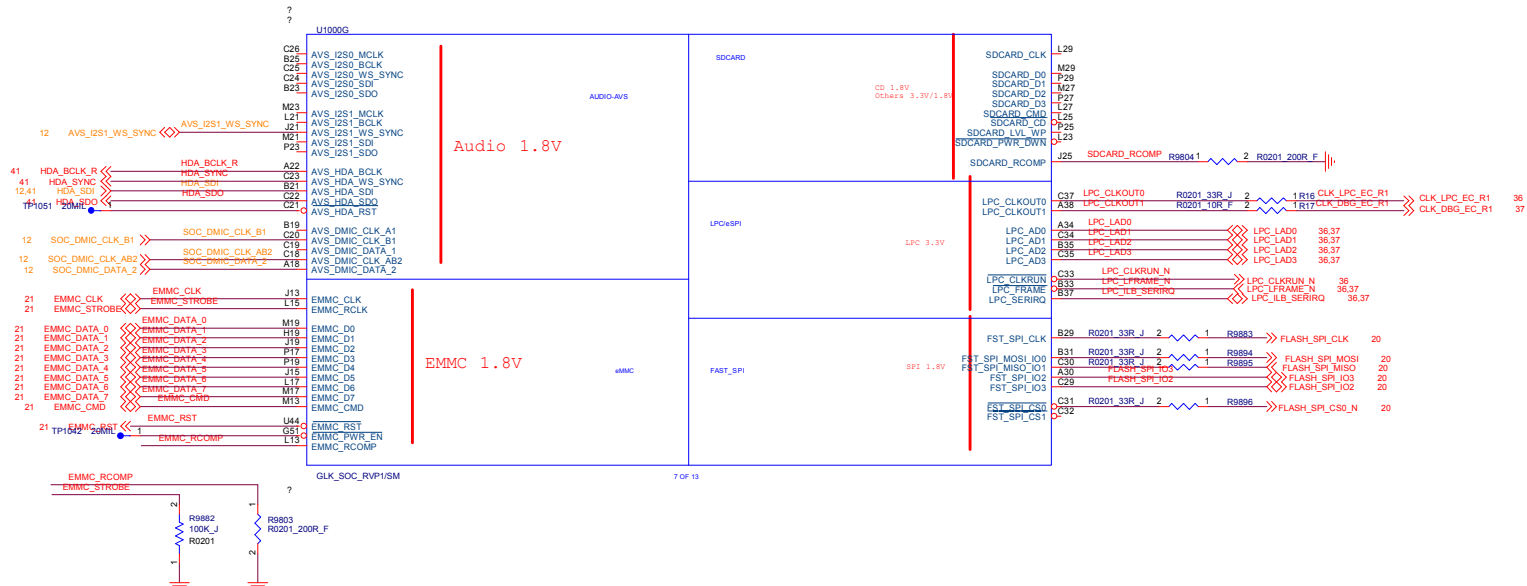
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Size: A4	Project Name: <b>MATEBOOK X</b>	REV: V1.0	
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<b>Page name: TABLE OF CONTENTS</b>			
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<b>Date:</b> Friday, June 16, 2019	<b>Sheet:</b> 2	<b>of</b> 44	





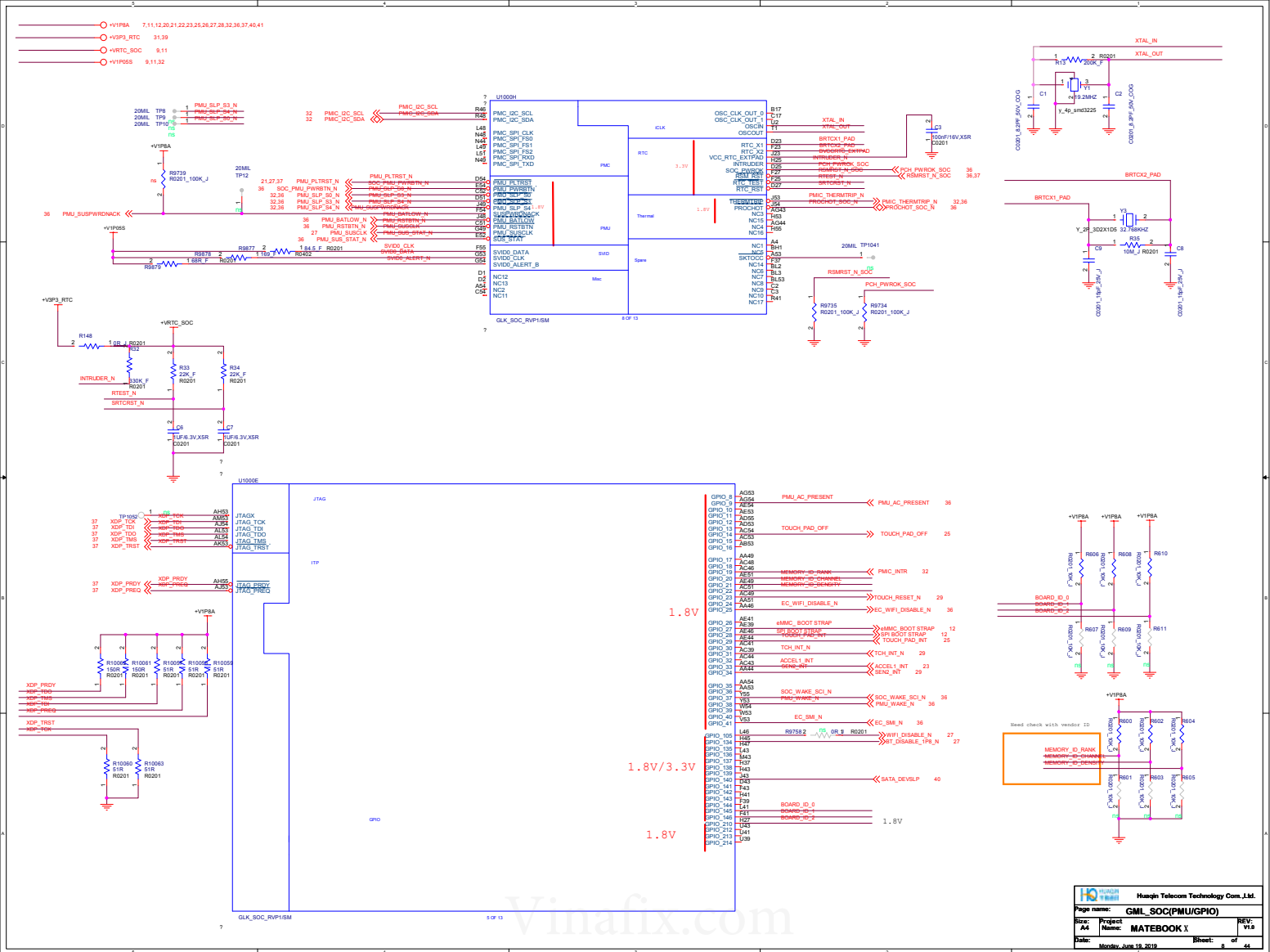


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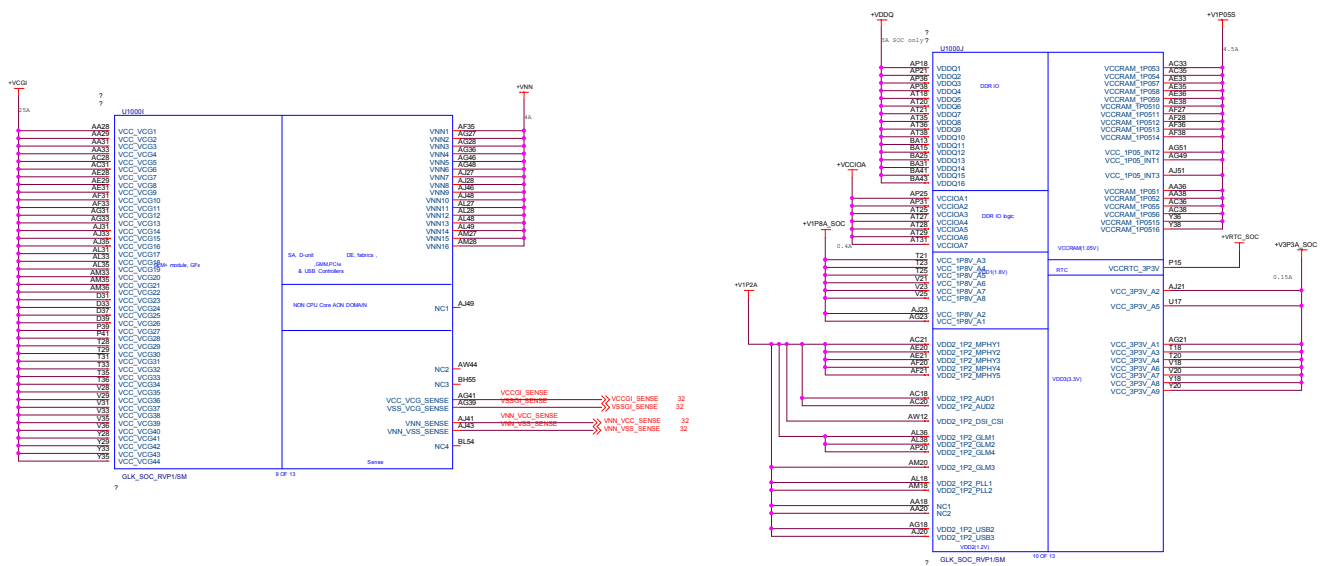
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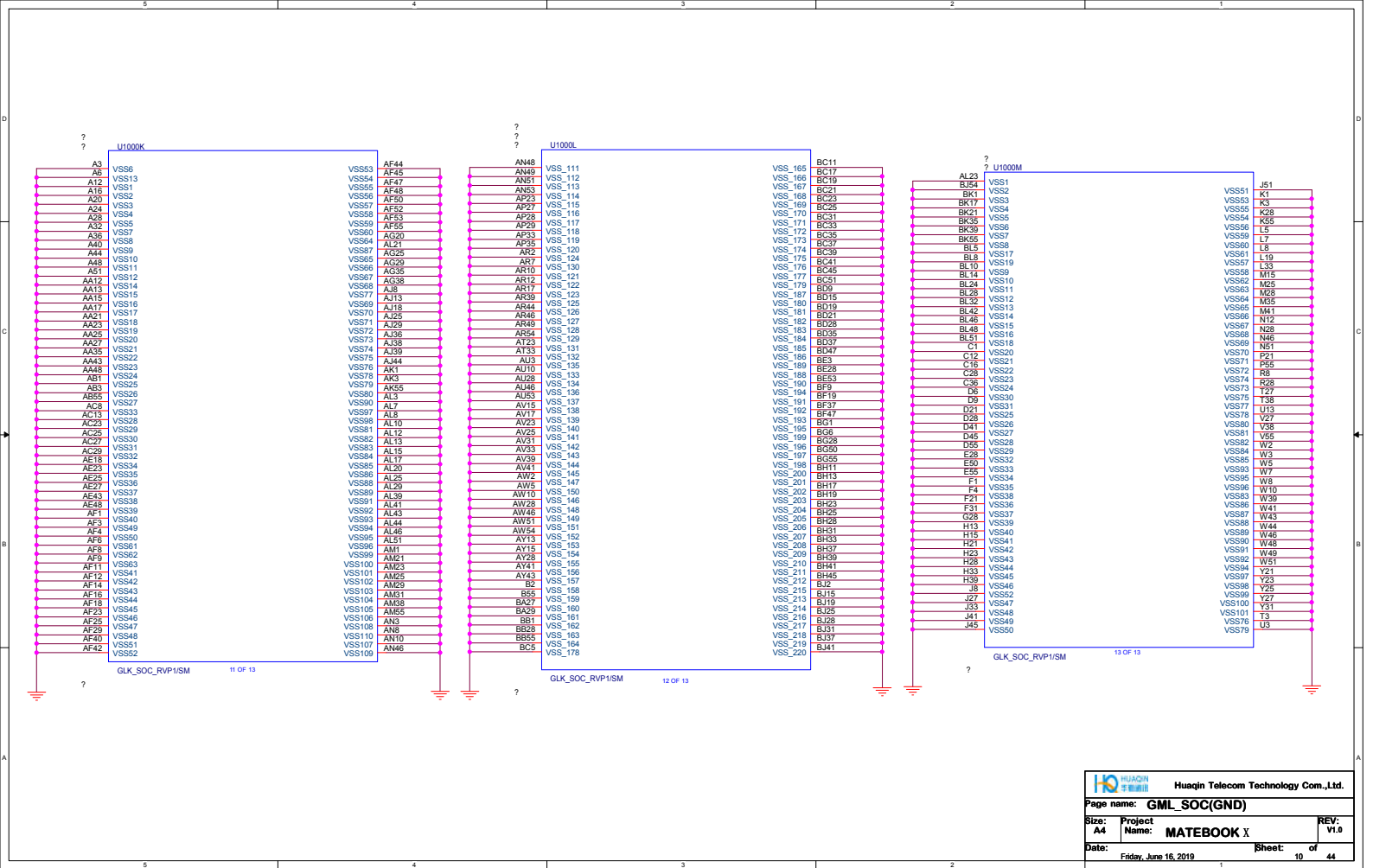




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VDD0	11.32
VDD0A	11.32, 14, 15, 16, 19, 32
VIPSA_SOC	11
VIPSA	11.32
VRTC_SOC	8.11
VIPSS	8.1, 13.2
VIPSA	11.32, 21, 23, 24, 25, 27, 28, 29, 31, 32, 36, 37, 41
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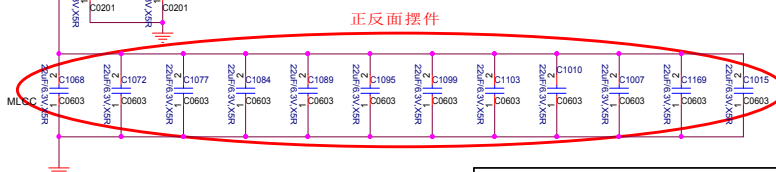
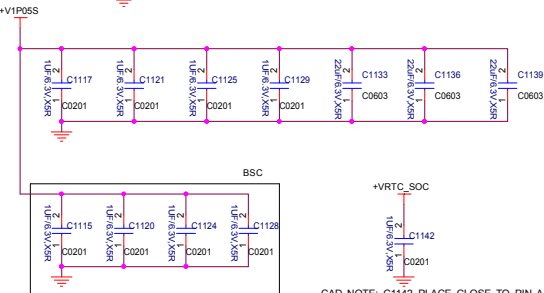
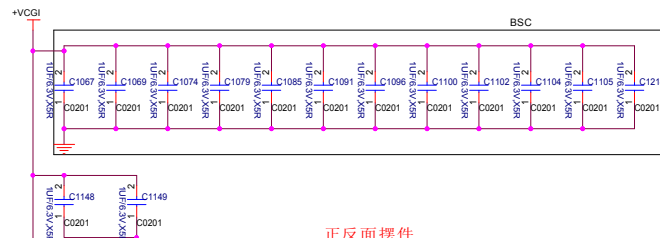
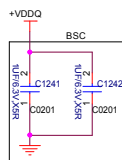
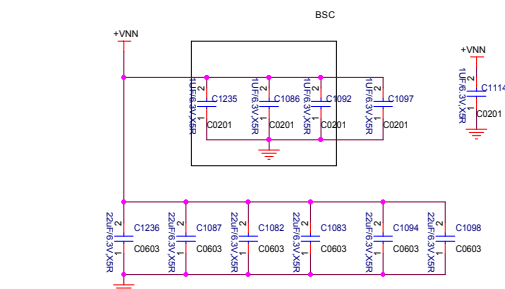
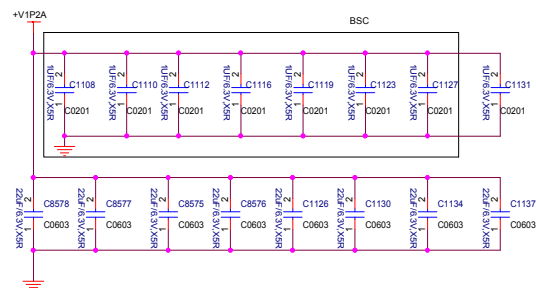
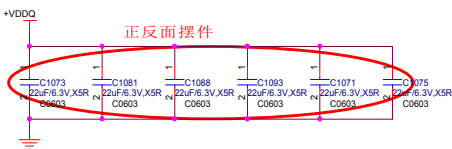
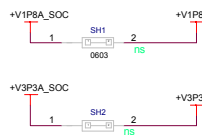
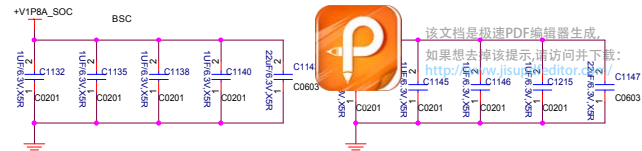
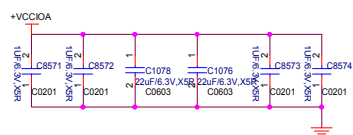


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+VNN 9.32  
 +VCC1A 9.32  
 +VDDQ 9.13,14,15,16,19,32  
 +V3P3A\_SOC 8.32 9  
 +V1P8A 8.32  
 +V1P8A\_SOC 9  
 +VRTC\_SOC 8.9  
 +V1P05S 8.9,32  
 +V1P8A 7.8,12,20,21,22,23,25,26,27,28,32,36,37,40,41  
 +V3P3A 20,21,23,24,25,27,28,29,31,32,36,37,41  
 +VCC1A 9



CAD NOTE: C1142 PLACE CLOSE TO PIN AA44

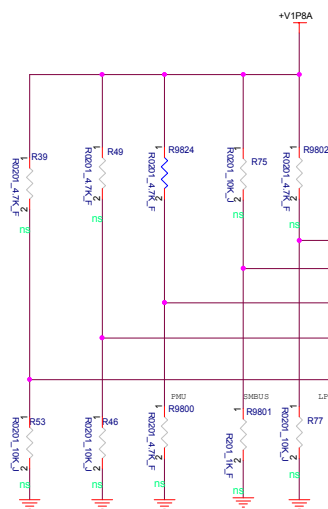
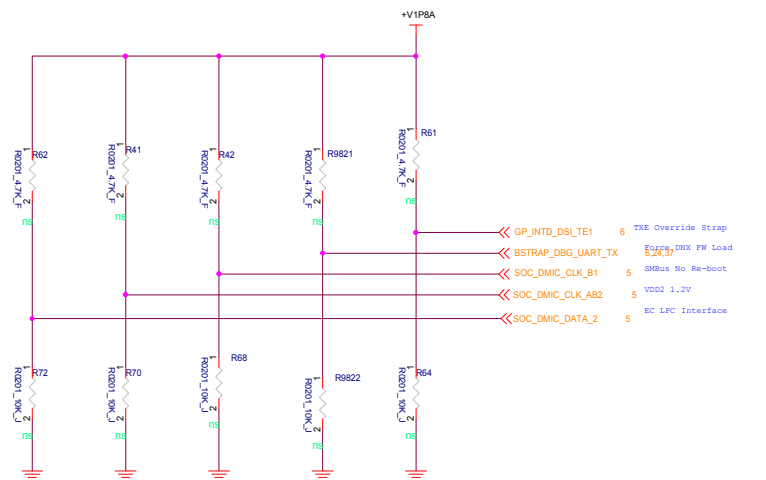
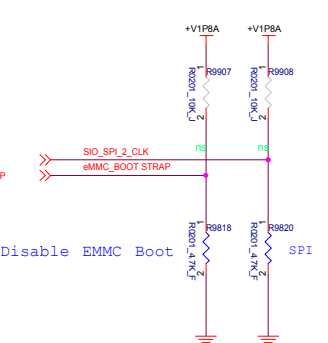
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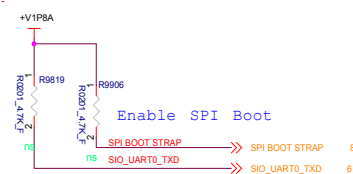
6 SIO\_SPL\_2\_CLK  
8 eMMC\_BOOT STRAP

Pull Down For Disable EMMC Boot

SPI as BOOT source

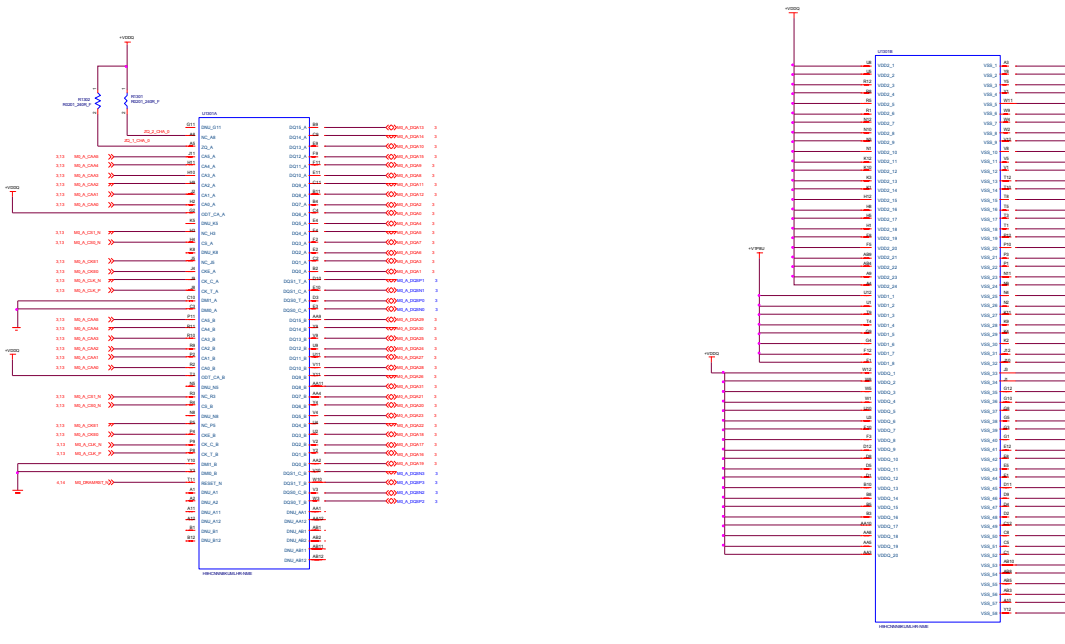


LPSS\_SPI0\_TXD 6 LPC 3.3V  
AVS\_I2S1\_WS\_SYNC 5 SMBus 3.3V  
HDA\_SDI 5.41 PMS1 Choose 1.8V  
LPSS\_UART2\_RTS\_N 6 SPI BIOS BOOT  
CNVLBR1\_DT 6 eSPI Flash Sharing Mode

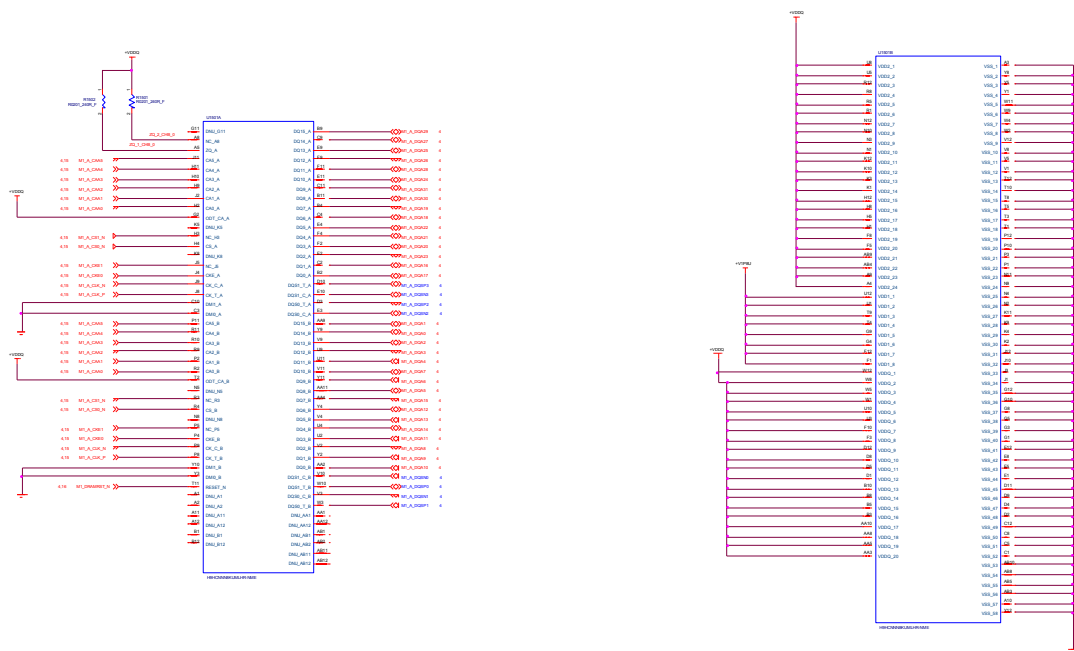


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Page name: GML_SOC(STRAP)			
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D

D

C

C

B

**B**

**A**

A



**Huaqin Telecom Technology Com.,Ltd.**

Page name: MEM\_DDR3L CH0 TERMINATIONS

Size:  
A4Project Name: **MATEBOOK X**

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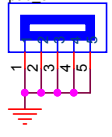
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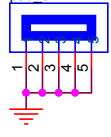
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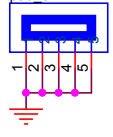
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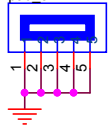
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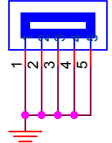
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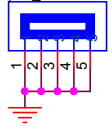
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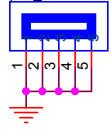
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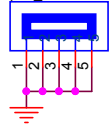
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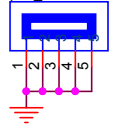
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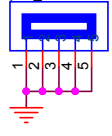
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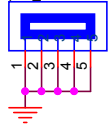
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Shield10  
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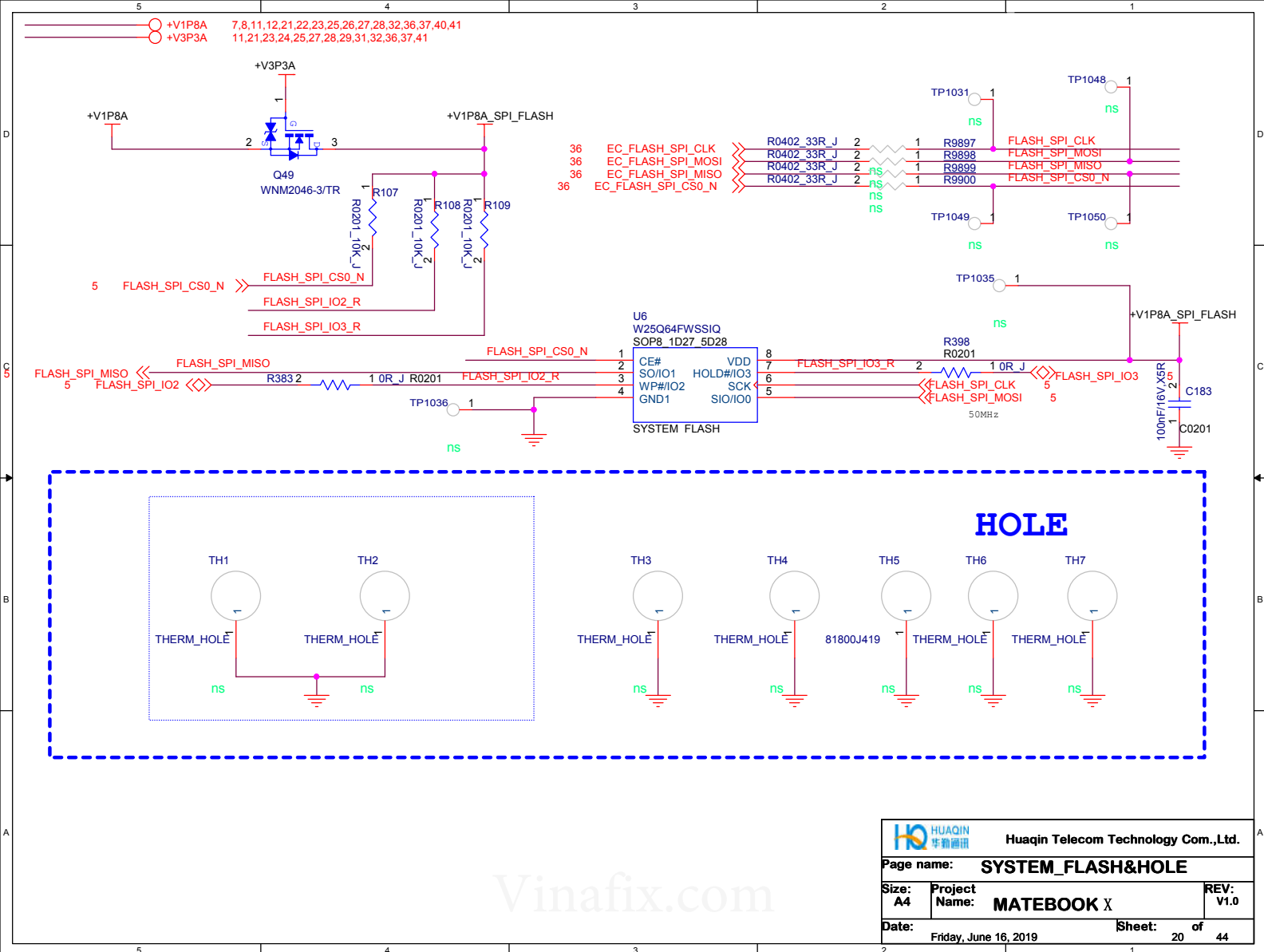
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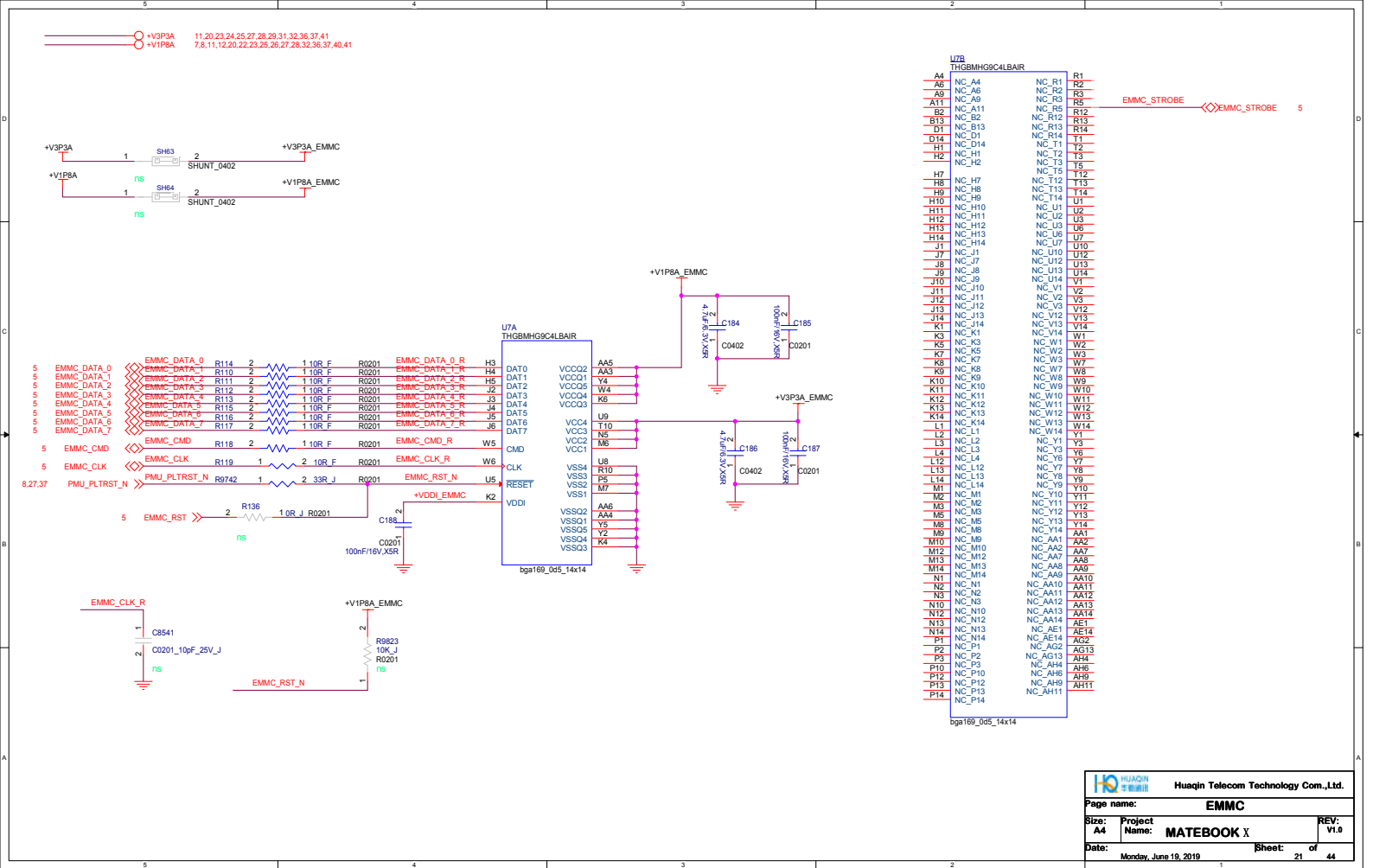


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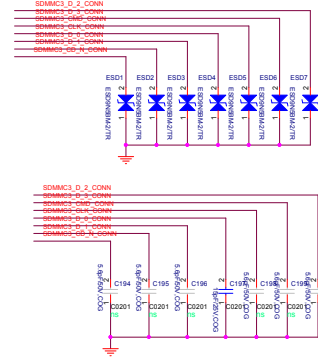
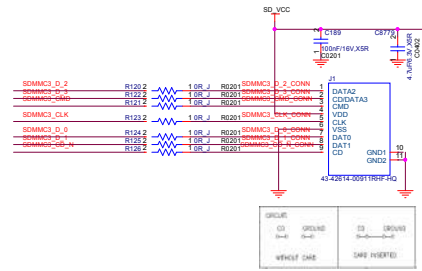
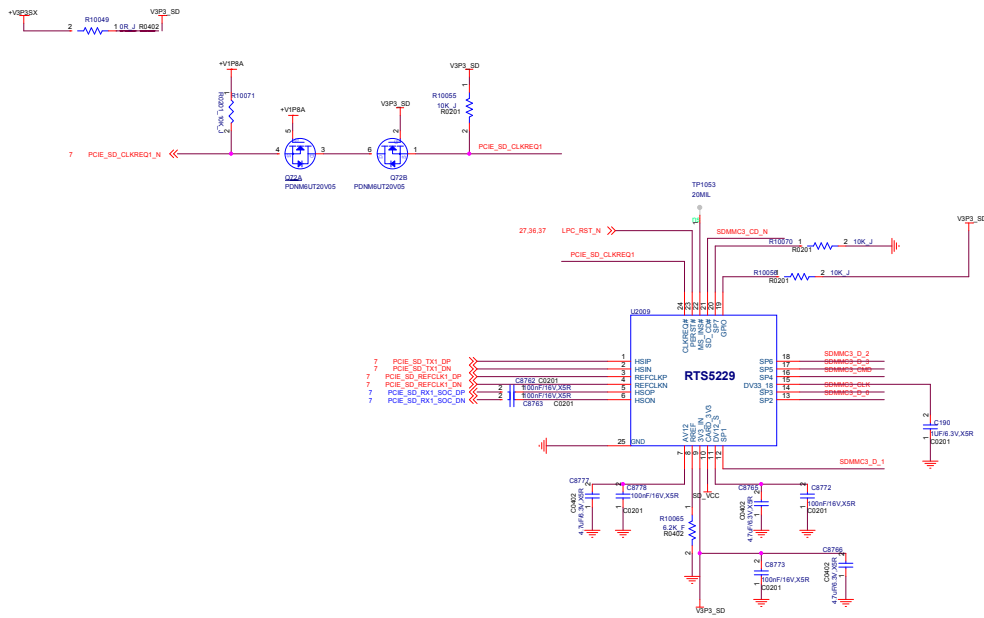






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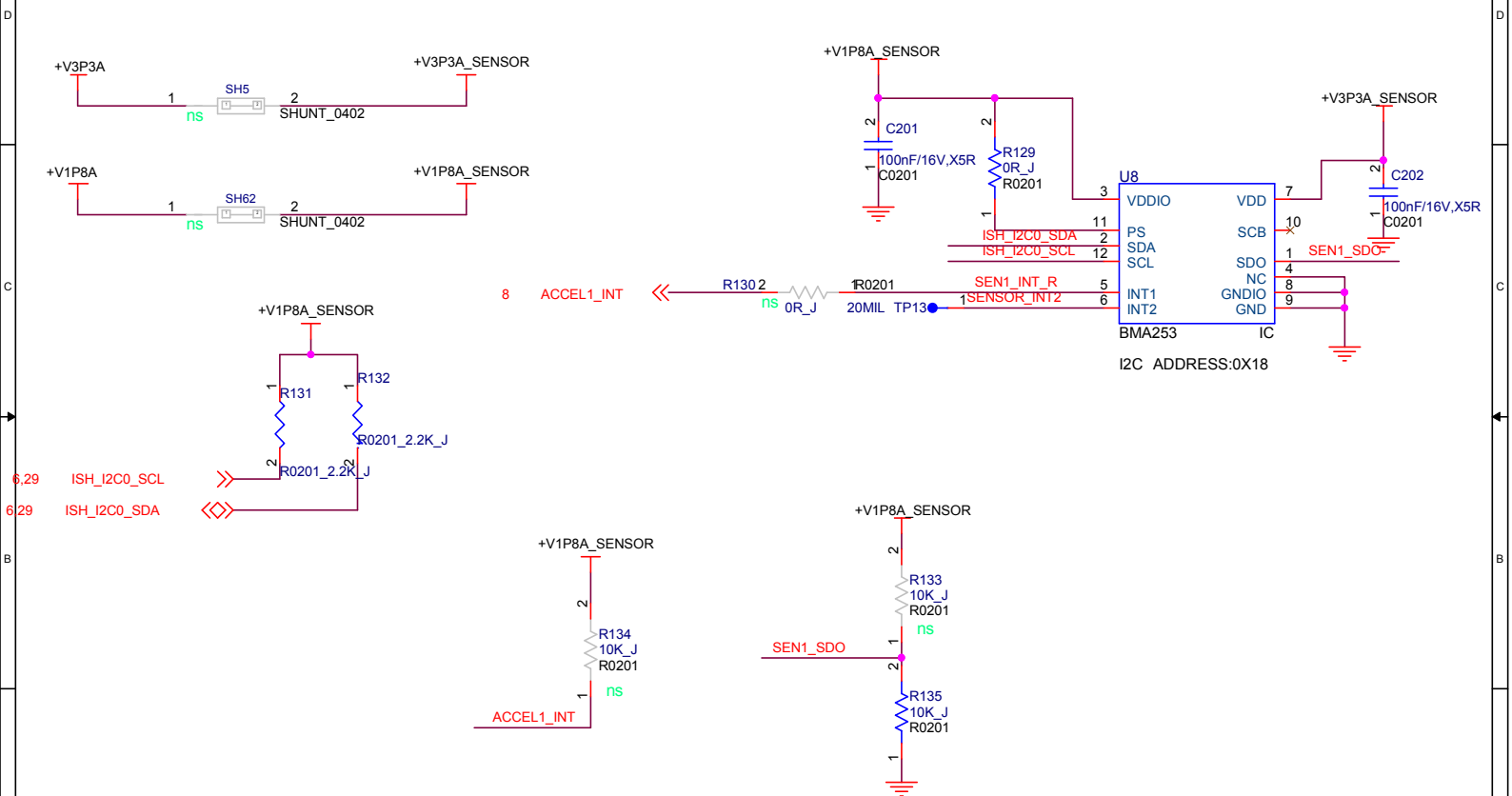



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+V3P3A 11,20,21,24,25,27,28,29,31,32,36,37,41  
 +V1P8A 7,8,11,12,20,21,22,25,26,27,28,32,36,37,40,41  
 +V3P3A\_SENSOR 29  
 +V1P8A\_SENSOR 29



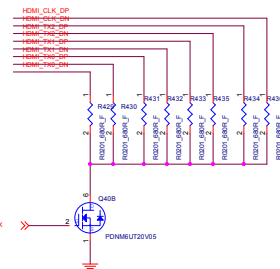
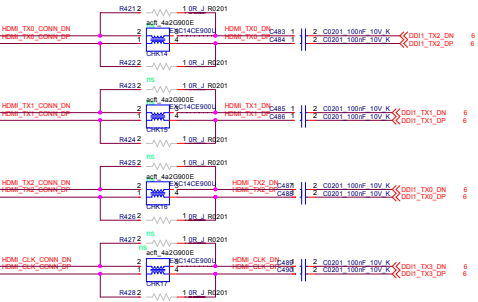
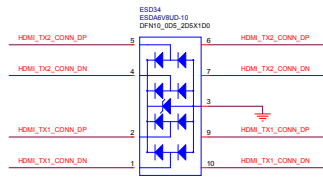
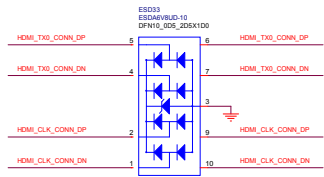
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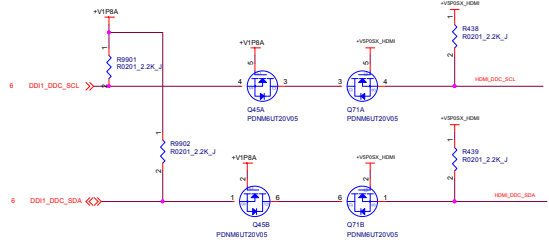
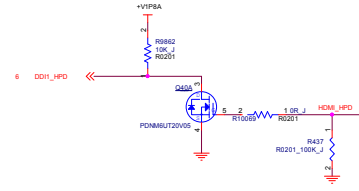
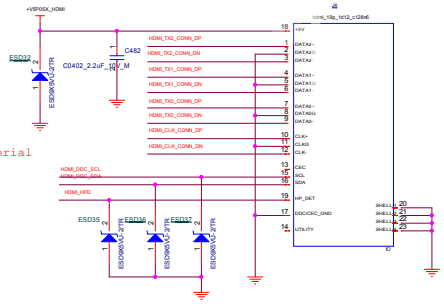



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 +V1P5A  
 24.31,32,41  
 7.8,11,12,20,21,22,23,25,27,28,32,36,37,40,41

26.31,36,41 EC\_SLP\_SX



26.31,36,41 EC\_SLP\_SX



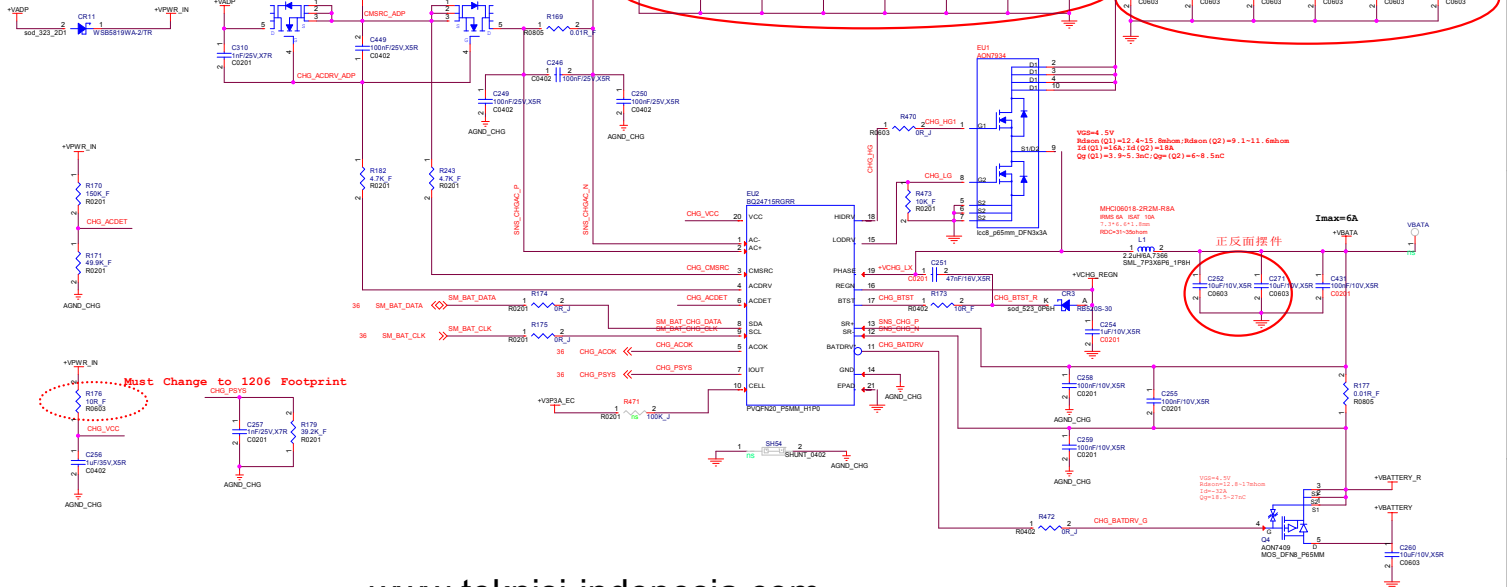
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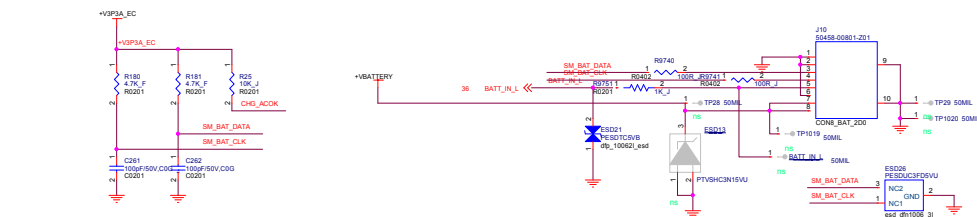




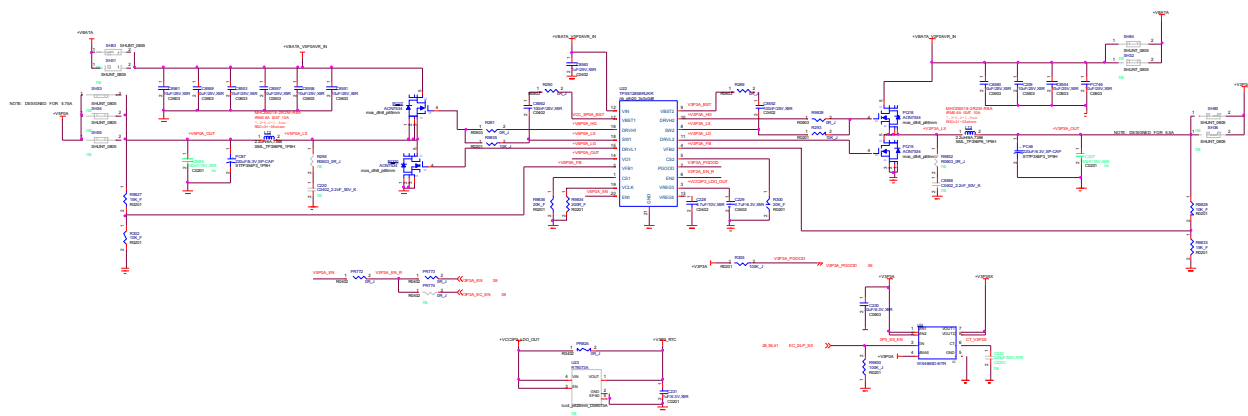




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B				
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B				B
A				A
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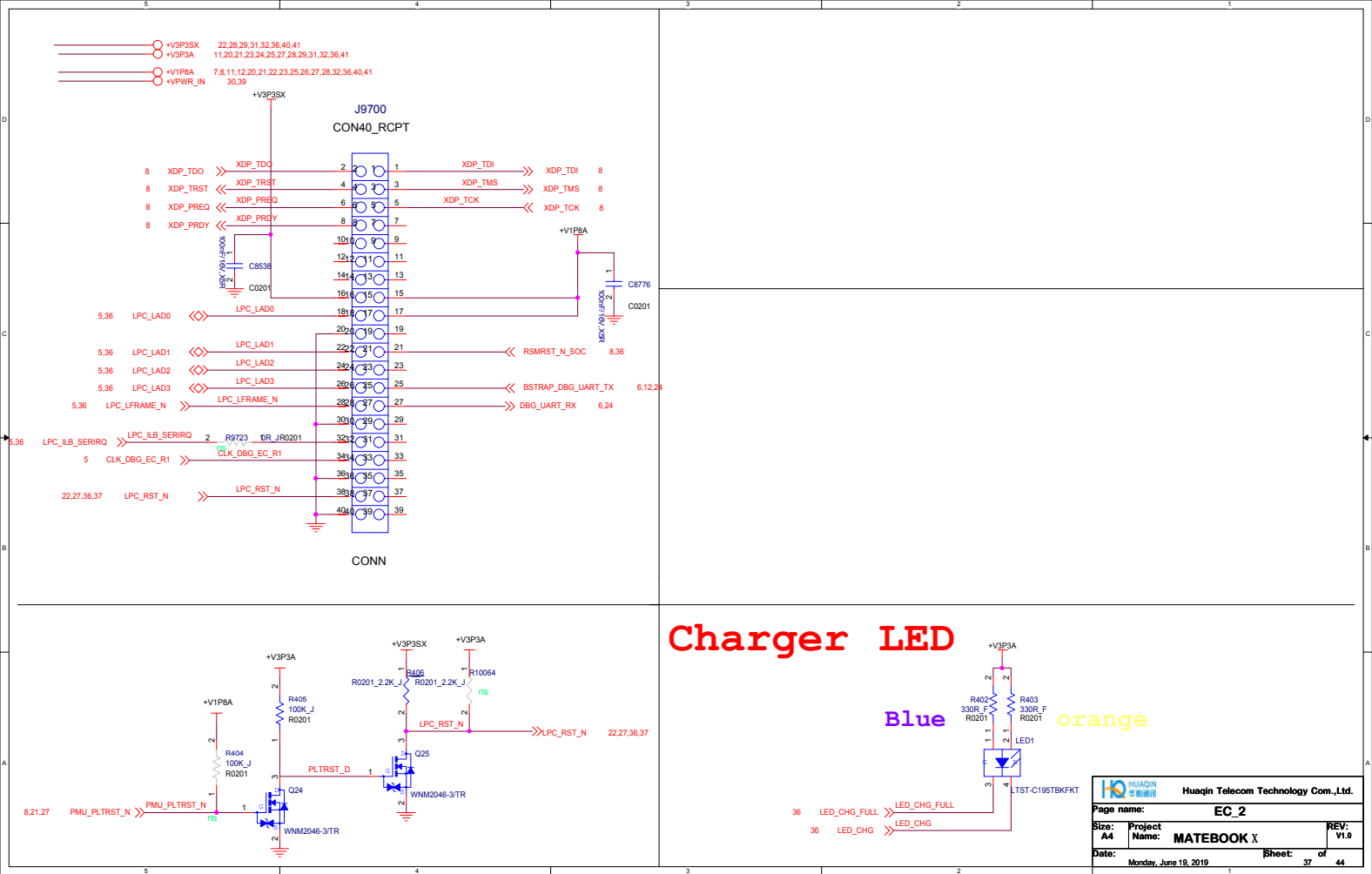
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BPAGE DRAWING

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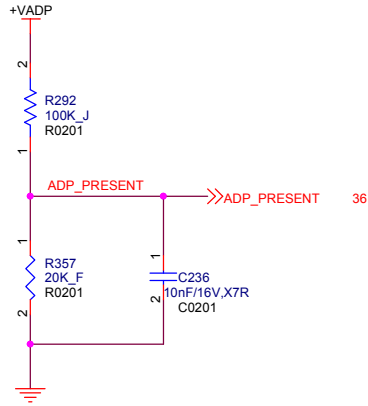
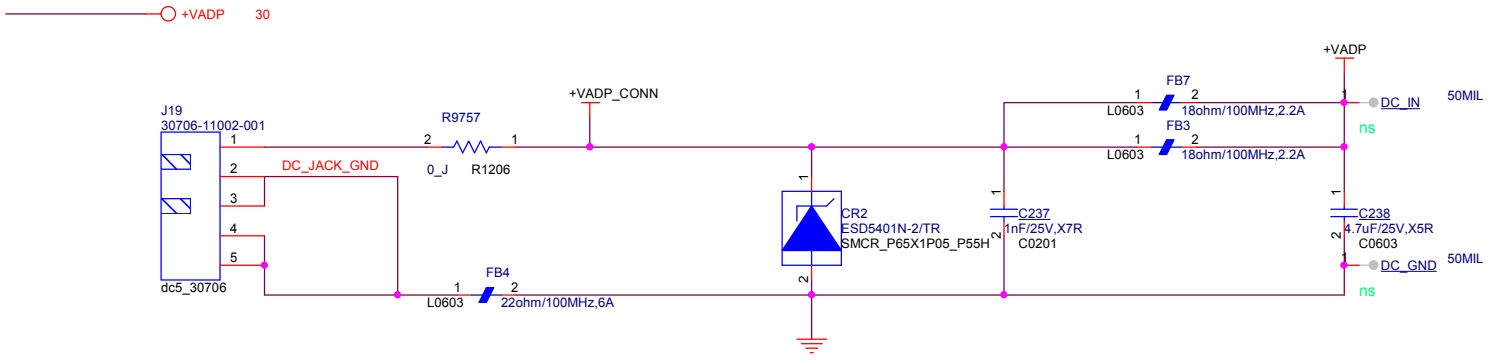
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D				D																
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		Huaqin Telecom Technology Com.,Ltd.																		
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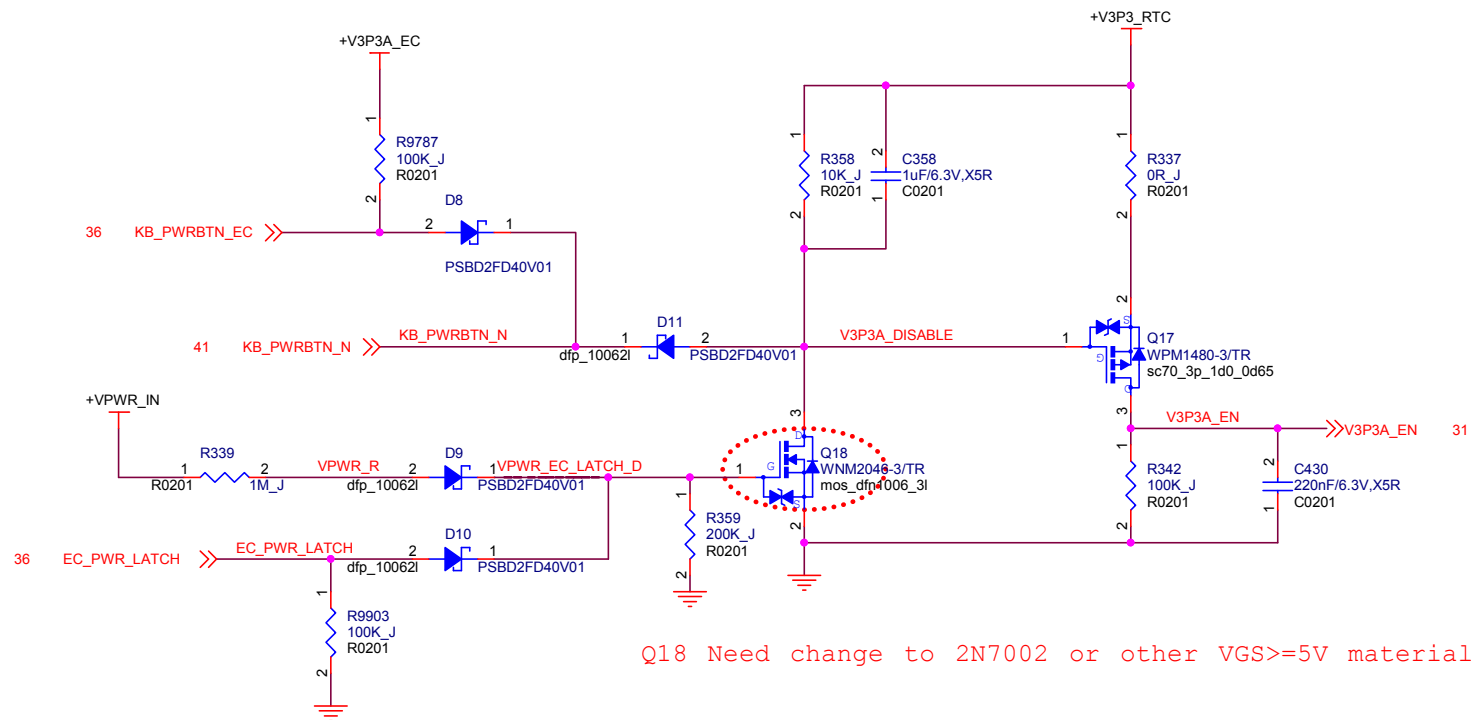
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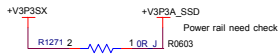
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 +VPWR\_IN 30



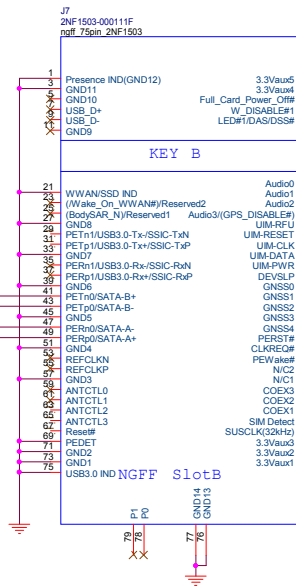
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+V3P3SX 22,28,29,31,32,36,37,41  
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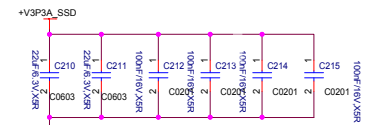
CAD NOTE: PLACE THESE AC CAPS CLOSE TO M2 CONN

7 SATA\_RXP0\_SSD\_DP << SATA\_RXP0\_SSD\_DP C246 1 2 C0201 10nF 16V K  
7 SATA\_RXN0\_SSD\_DN << SATA\_RXN0\_SSD\_DN C247 1 2 C0201 10nF 16V K  
7 SATA\_TXN0\_SSD\_DN << SATA\_TXN0\_SSD\_DN C248 1 2 C0201 10nF 16V K  
7 SATA\_TXP0\_SSD\_DP << SATA\_TXP0\_SSD\_DP C249 1 2 C0201 10nF 16V K

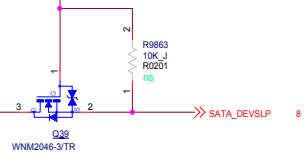


+V3P3A\_SSD

SATA\_DEVSLPD



+V1P8A



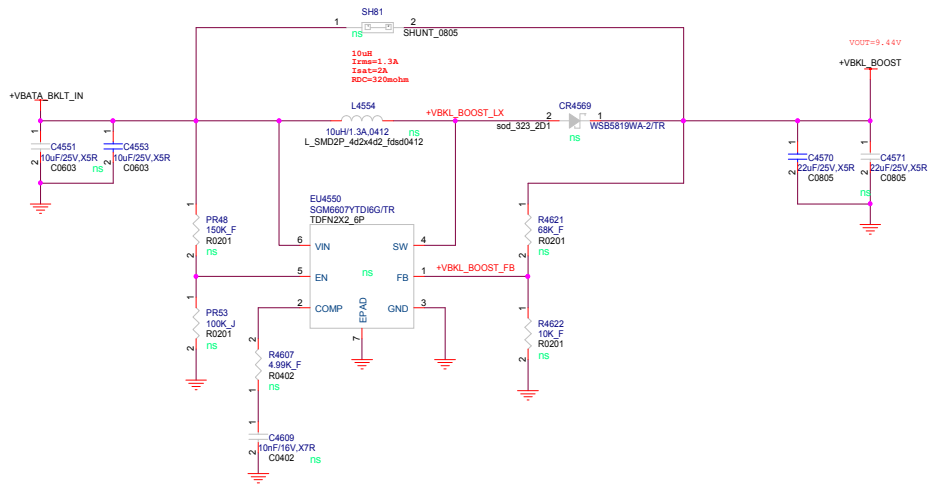
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SSD	
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Interface	Part	SOC_I2C		EC_I2C	Address
Accelerometer1	BMA253	ISH_I2C0_SDA ISH_I2C0_SCL	I2C_5		
Accelerometer2	BMA253	ISH_I2C0_SDA ISH_I2C0_SCL	I2C_5		
Touch Screen	TBD	TCH_I2C_SDA TCH_I2C_SCL	I2C_3		
Touch PAD	TBD	TOUCH_PAD_SDA TOUCH_PAD_SCL	I2C_4	TOUCH_PAD_SDA TOUCH_PAD_SCL	
Audio Codec	ALC256	I2C_CLK I2C_DATA	I2C_1		
Battery Charger	BQ24715			SM_BAT_DATA SM_BAT_CLK	

+VBATA\_BKLT\_IN 28  
+VBKL\_BOOST 28



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